

FIG. 1

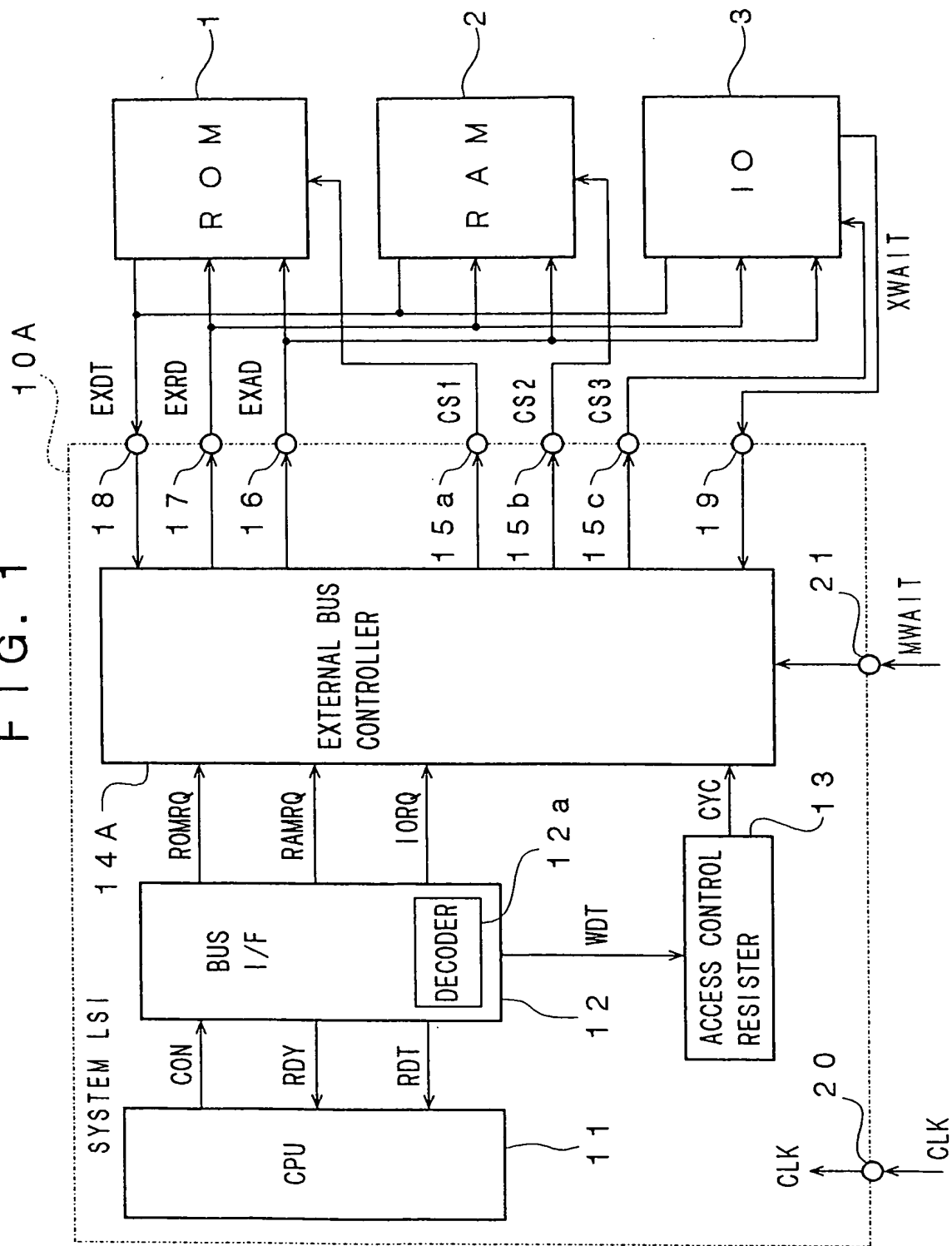


FIG. 2  
PRIOR ART

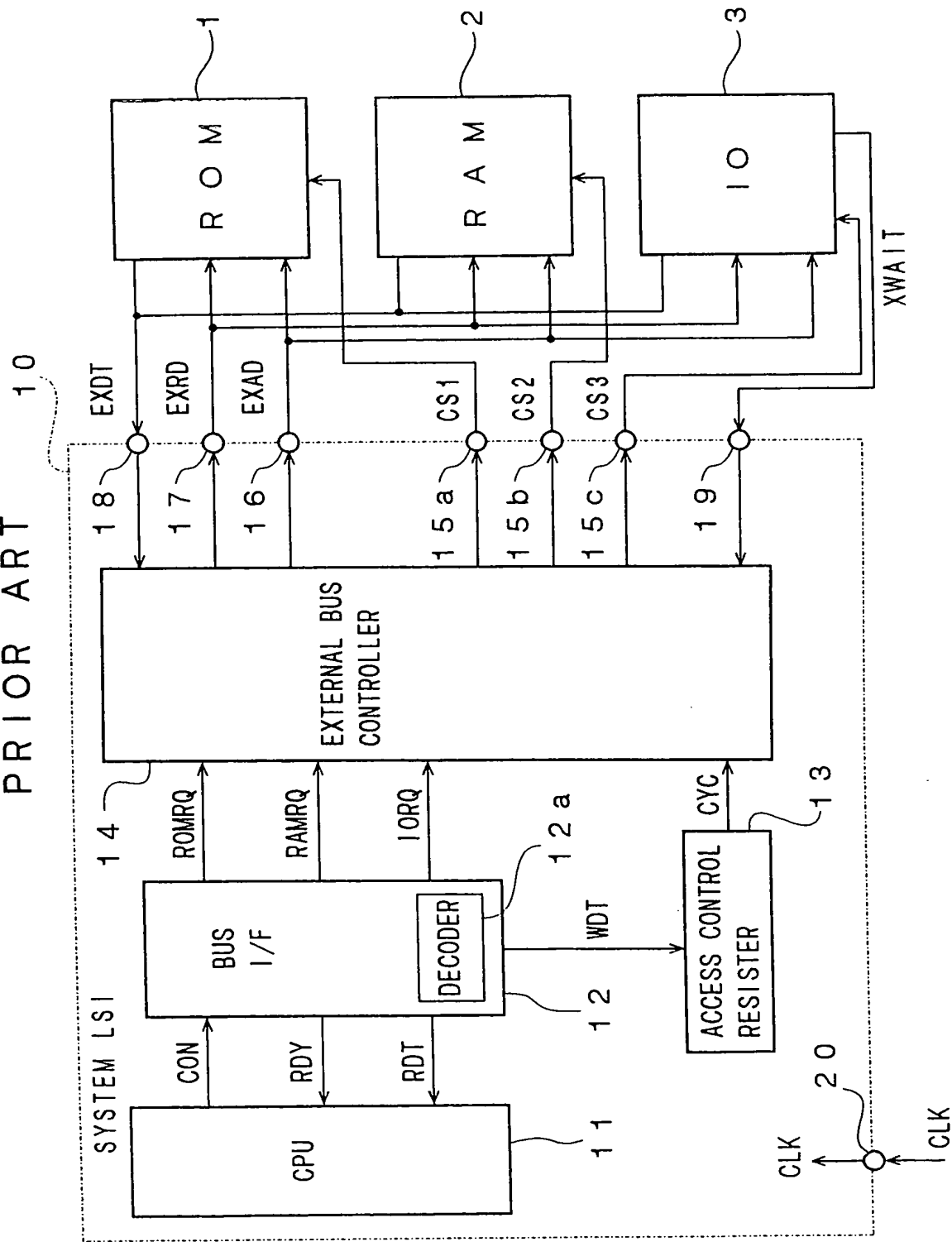


FIG. 3

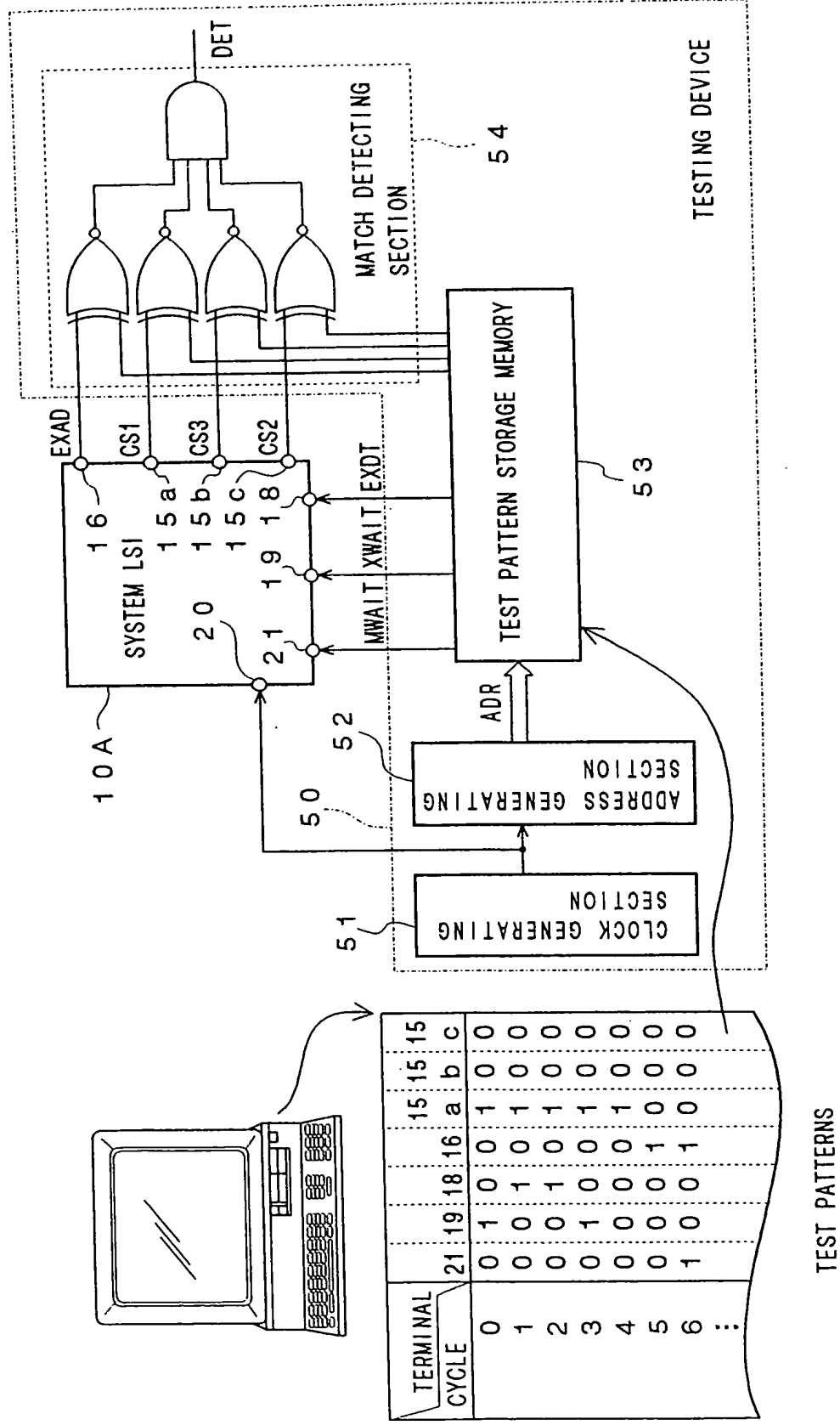


FIG. 4

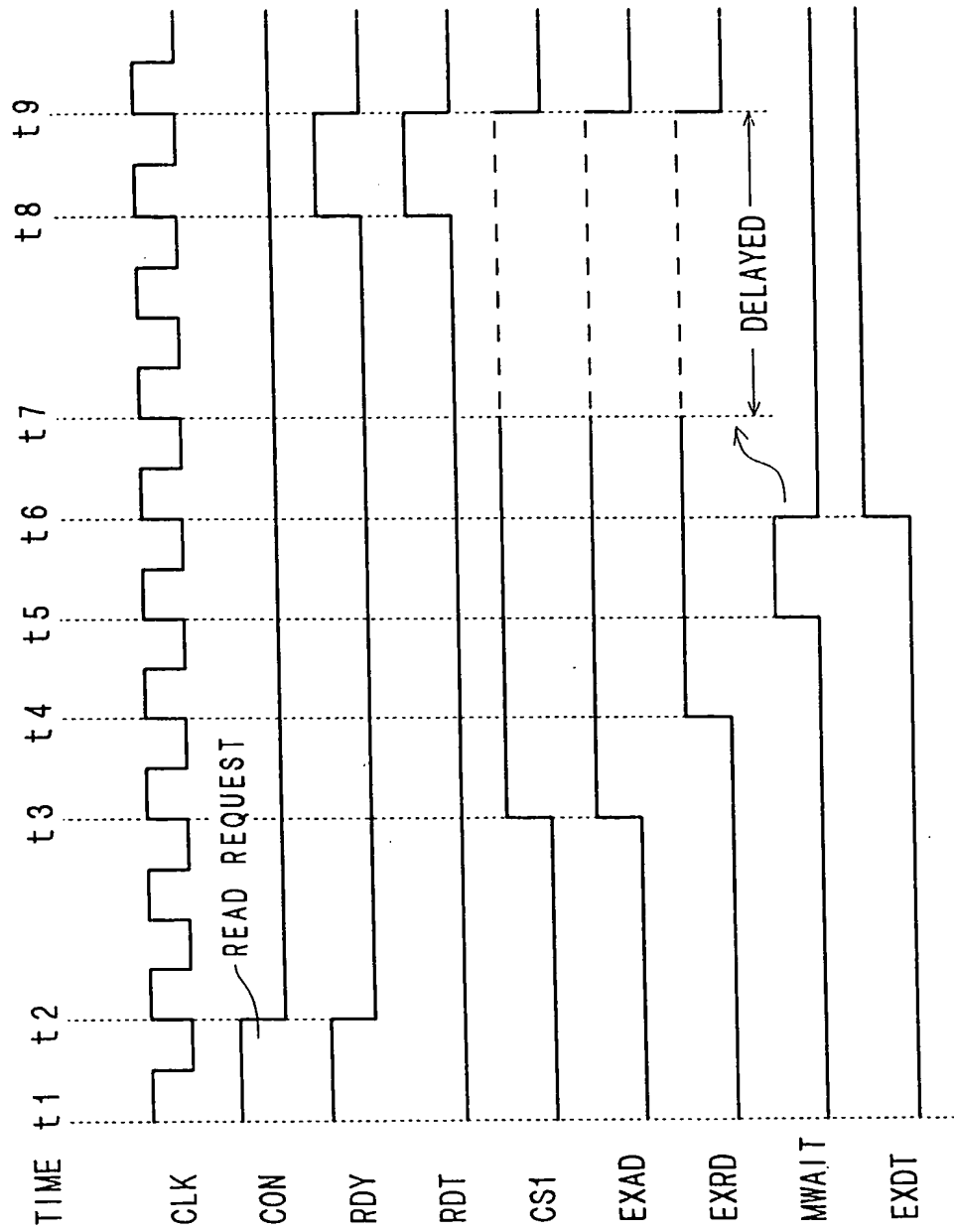


FIG. 5

10B

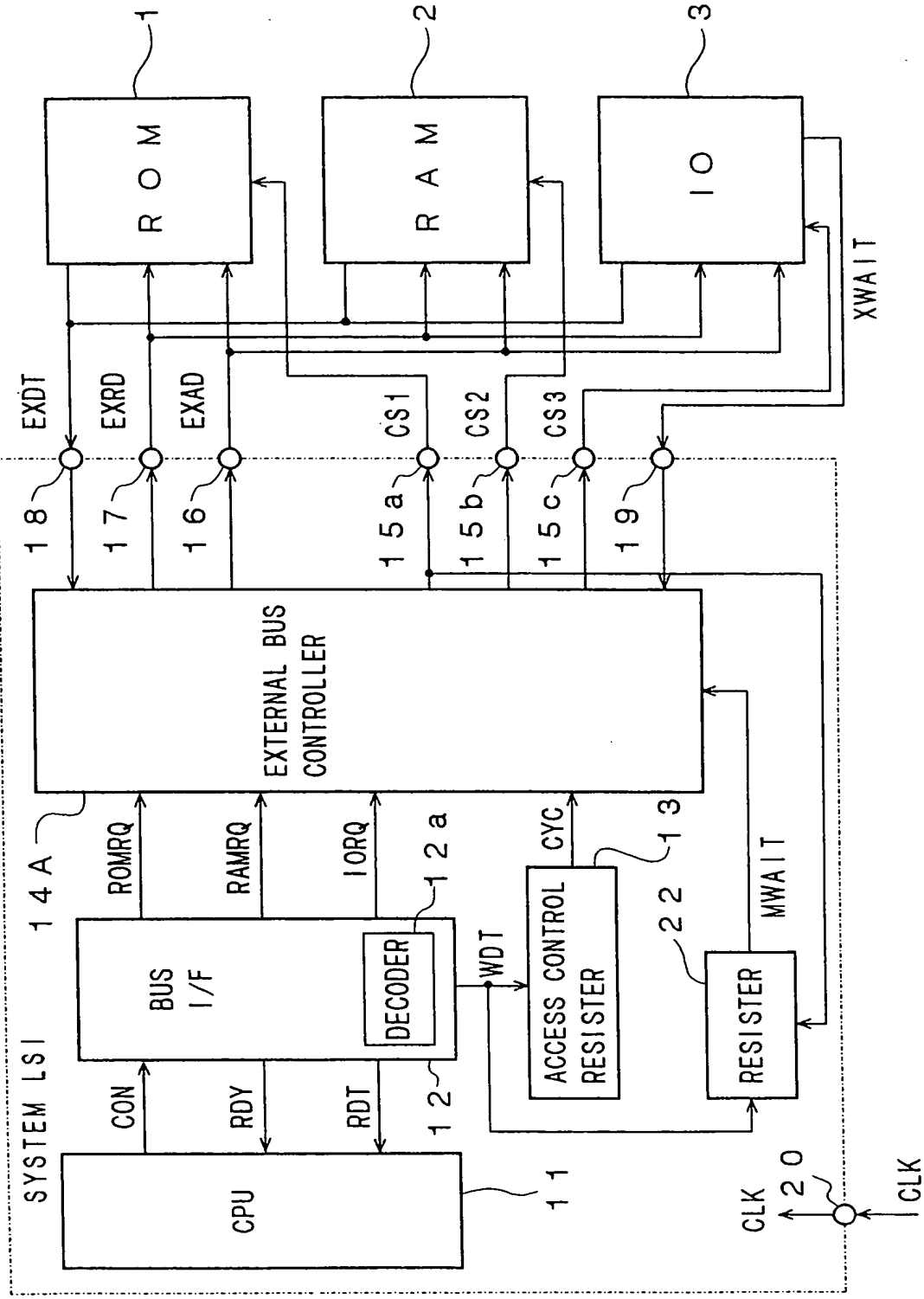


FIG. 6

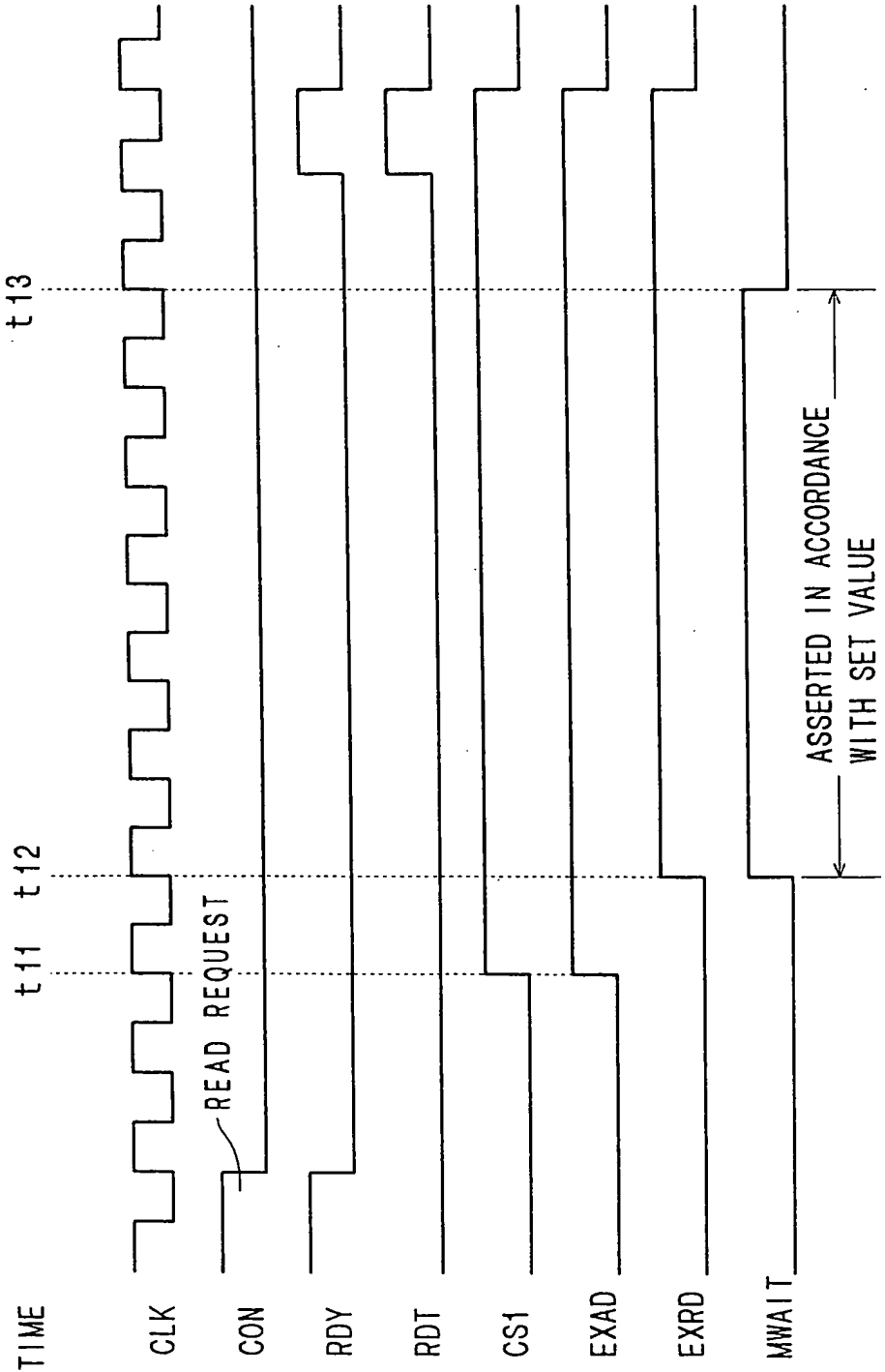


FIG. 7

10C

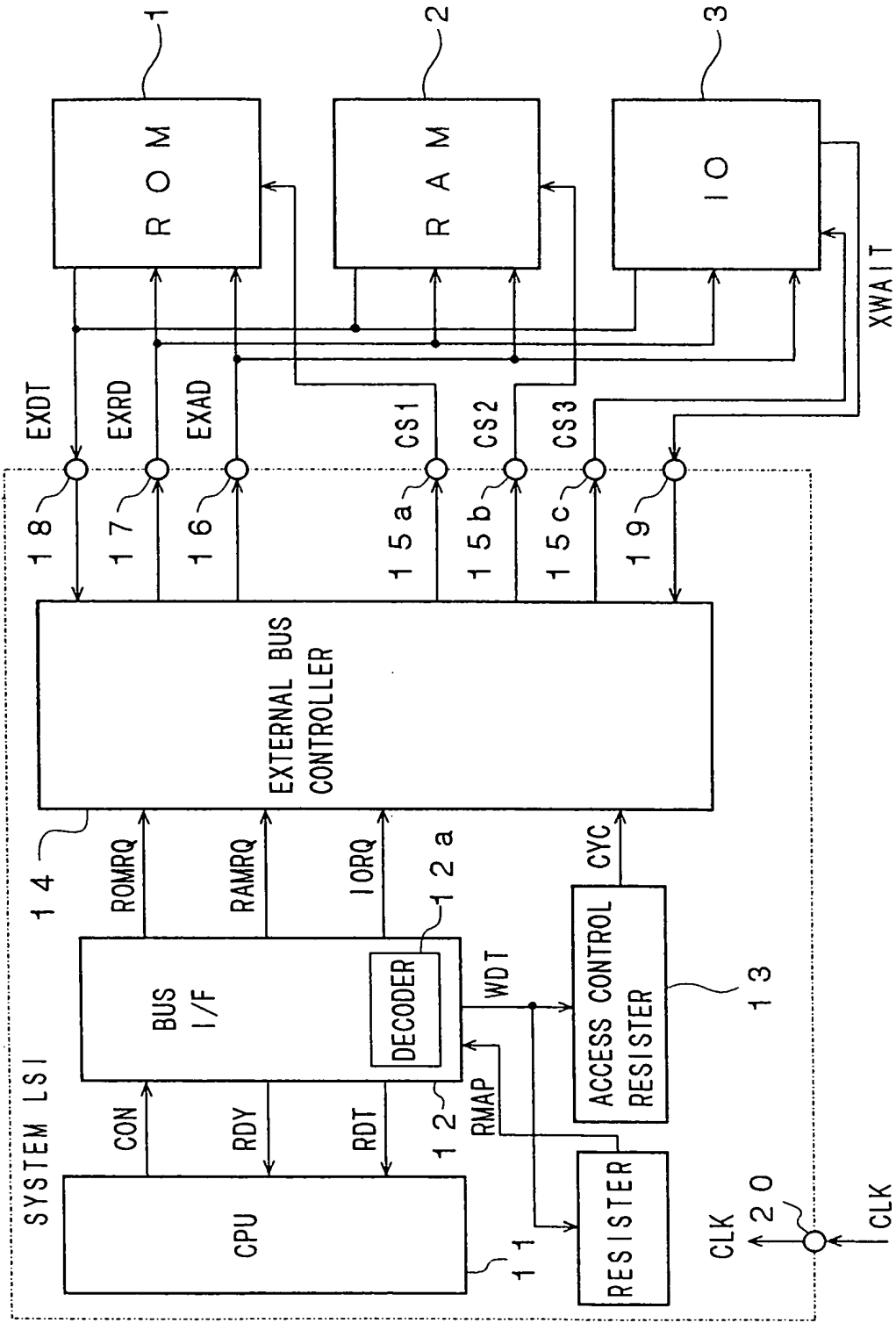


FIG. 8

CON	RMAP	ROMRQ	RAMRQ	IORQ
ROM ADDRESS	—	1	0	0
RAM ADDRESS	—	0	1	0
IO ADDRESS	—	0	0	1
0 BANK	0	1	0	0
0 BANK	1	0	0	1